Tomasulo-style DLX

Instruction	Issue	Execute	Write

	Busy	Address	Qi
LD1			
LD2			
LD3			
ST1			
ST2			
ST3			

	Busy	Op	Vj	Vk	Qj	Qk
FPU1						
FPU2						
FPU3						
FPU4						

	Qi
F0	
F2	
F4	
F6	
F8	
F10	
F12	
F14	
F16	
F18	
F20	
F22	
F24	
F26	
F28	
F30	