

Homework 4 (due Wed. 7/21)

1. Contrast the AMPED model from the Flash paper and the MT-server model. Which specific issues from the MT-model does the AMPED model address and how.
2. Given the abstraction and operations supported in the L3 microkernel, describe how would you implement a device driver. Outline the operations involved in handling an interrupt with your device driver.
3. Caching is an important optimization technique. Describe three potential locations for caching in a distributed file system. Briefly discuss the advantages and disadvantages of each location.