

14th IEEE International Conference on High Performance Computing



H i P C 2 0 0 7

Goa, India

December 18-21, 2007

<http://www.hipc.org>

IMPORTANT DATES

April 7, 2007	Paper Submission Begins
May 7, 2007	Paper Submission Ends
May 14, 2007	Workshop Proposals Due
May 21, 2007	Tutorial Proposals Due
July 16, 2007	Accept/Reject Notification
August 20, 2007	Camera-Ready Paper Submission
September 24, 2007	Poster/Presentation Summary Due
November 7, 2007	Early Registration Deadline

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Applications: Sivan Toledo, Tel-Aviv Univ., Israel
Architecture: Peter J. Varman, Rice Univ., USA
Communication Networks: Dhableswar K. Panda, Ohio State Univ., USA
Mobile and Sensor Computing: Ahmed Helmy, Univ. of Florida, USA
Systems Software: Manish Gupta, IBM Corp., India

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CALL FOR PAPERS

The 14th annual IEEE International Conference on High Performance Computing (HiPC 2007) will be held in Goa, India, during December 18-21, 2007. It will serve as a forum to present the current work by researchers from around the world, and act as a venue to provide stimulating discussions and highlight high performance computing activities in Asia. The conference has a history of attracting participation from reputed researchers from all over the world.

HiPC 2007 will focus on the design and analysis of high performance computing and networking systems and their scientific, engineering, and commercial applications. In addition to technical sessions consisting of contributed papers, the conference will include invited presentations, a poster session, tutorials, and vendor presentations. Further information about HiPC 2007 conference and HiPC series of meetings is available at <http://www.hipc.org>.

Authors are invited to submit original unpublished manuscripts that demonstrate current research in all areas of high performance computing including design and analysis of parallel and distributed systems, networking systems, embedded systems, and their applications in scientific, engineering, and commercial areas. Topics of interest include but are not limited to:

- High-Performance Computing
- Parallel and Distributed Algorithms
- Parallel Languages and Programming Environments
- Load Balancing, Scheduling and Resource Management
- Fault-Tolerant Algorithms and Systems
- Scientific/Engineering/Commercial Applications and Workloads
- Emerging Applications such as Biotechnology and Nanotechnology
- Cluster and Grid Computing
- Peer-to-peer Algorithms and Networks
- Heterogeneous Computing
- Wireless and Mobile Computing
- Communication/Sensor Networks and Embedded Applications
- Interconnection Networks and Architectures
- Scalable Servers and Systems
- Power-Efficient and Reconfigurable Architectures
- Compiler Technologies for High-Performance Computing
- Software Support and Advanced Micro-architecture Techniques
- Operating Systems for Scalable High-Performance Computing

Best paper awards, sponsored by Infosys, will be given for outstanding contributed papers in two areas: (a) Algorithms and Applications, and (b) Systems. Further details about call for posters, workshops, tutorials, exhibits, as well as submission guidelines will be available at the conference website <http://www.hipc.org>.



Sponsored by: IEEE Computer Society Technical Committee on Parallel Processing (TCPP), ACM Special Interest Group on Computer Architecture (SIGARCH), European Association for Theoretical Computer Science, IFIP Working Group on Concurrent Systems, National Association of Software and Service Companies (NASSCOM), and Manufacturers' Association for Information Technology (MAIT).



[See overleaf for HiPC 2007 Organization]

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HiPC 2007 ORGANIZATION

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