

## WISA06 Final Program

Sunday Feb. 12th 2006

01:30pm-03:00pm	<b>Session 1: Reconfigurability and Fault Tolerance</b>
	<b>General Overview of an Adaptive Dynamic Extensible Processor</b> Hamid Noori (Kyushu University), Kazuaki Murakami (Kyushu University), Koji Inoue (Kyushu University)
	<b>A Dynamically Reconfigurable Virtual Instrument</b> Gerd Van den Branden (Erasmushogeschool Brussel), Geert Braeckman (Erasmushogeschool Brussel), Abdellah Touhafi (Erasmushogeschool Brussel), Erik Dirx (Vrije Universiteit Brussel)
	<b>VOLTaiRE: Low-Cost Fault Detection Solutions for VLIW Microprocessors</b> Smitha Shyam (University of Michigan), Sujay Phadke (University of Michigan), Benjamin Lui (University of Michigan), Hitesh Gupta (University of Michigan), Valeria Bertacco (University of Michigan), David Blaauw (University of Michigan)
03:00pm-03:30pm	Break
03:30pm-06:00pm	<b>Session 2: Micro-Architectural Issues</b>
	<b>Cycle-Accurate Microarchitecture Performance Evaluation</b> Richard Hough (Washington University), Phillip Jones (Washington University), Scott Friedman (Washington University), Roger Chamberlain (Washington University), Jason Fritts (Washington University), John Lockwood (Washington University), Ron Cytron (Washington University)
	<b>A Preliminary Evaluation of Timing-Speculative Instruction Collapsing</b> Toshinori Sato (Kyushu Institute of Technology), Akihiro Chiyonobu (Kyushu Institute of Technology)
	<b>Self optimizing finite State Machines for Confidence Estimators</b> Sandra Jackson (Cornell University), Martin Burtscher (Cornell University)
	<b>Phase-based adaptive branch predictor: Seeing the forest for the trees</b> Karthik Jayaraman (University of Wisconsin-Madison), Vivek Shrivastava (University of Wisconsin-Madison), Brian Pellin (University of Wisconsin-Madison), Mikko H. Lipasti (University of Wisconsin-Madison)
<b>Instruction-window Power Reduction Using Data Dependence Metric</b> Ziad Youssfi (Michigan State University), Michael Shanblatt (Michigan State University)	