

Vikram KrishnamurthyApt 5028, 470, 16th Street NW, Atlanta, GA 30363

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Objective	To pursue a career in Computer Systems leveraging acquired skills in the area of design and programming	
Education	Georgia Institute of Technology, Atlanta – MS Computer Science (GPA 3.83)	May 2011
	BMS College of Engineering, Bangalore, India - Bachelor of Engg in CS (GPA 3.75)	June 2007
Professional Experience	Travelport <i>Junior Architect, Global Delivery Systems</i>	Atlanta, US May 2010-Aug 2010
	<ul style="list-style-type: none"> Implementing the caching model for the Travelport distributed framework, providing POC for the same Implementing the <i>in-memory</i> data-store using REDIS with support for persistence and replication Involved in researching various NoSQL solutions and in-memory solutions for feasibility analysis 	
	EMC Data Storage Private Systems Ltd <i>Software Engineer, Content Mgmt and Archiving</i>	Bangalore, India Sep 2007-Jul 2009
	<ul style="list-style-type: none"> Involved in development and Performance benchmarking of WebPublisher Designed and implemented features for workflows, authorization and content synchronization Awarded "Meteor Reward", towards most valuable contributor of the team in EMC2, Mar 2008 	
	Hewlett Packard <i>Student Project Intern, System Technology Software Division</i>	Bangalore, India Jan 2007-Apr 2007
	<ul style="list-style-type: none"> Designed & implemented "MP Based Diagnostic Framework for HP Servers", an OS Absent solution Winner of HP Labs "<i>Best Undergraduate Research Project</i>" in India for the project 	
Research Experience	Systems Research Group (CERCS), Georgia Tech 'Efficient caching mechanisms' under Prof. Karsten Schwan	Atlanta, US Jul 2010-Present
	<ul style="list-style-type: none"> Design of caching solutions for enterprise application with focus on integrating in-memory solutions Framework for utilizing Redis with support for data persistence, sharding and failover 	
	'Project GViM' under Prof. Karsten Schwan	Jan 2010-Present
	<ul style="list-style-type: none"> Design and implementing asynchronous I/O support for effective data movement in GViM Aimed towards effective resource management of accelerators across GPU Systems 	
Projects	Interprocess Communication using Shared Memory [C]	
	<ul style="list-style-type: none"> Designed and implemented multithreaded library which uses shared ring buffer to provide services which enables blocking calls and QOS primitives 	
	Middleware for AwareHome - ROS [C++]	
	<ul style="list-style-type: none"> Designed and implemented middleware at Aware home using Robotic Operating System as the core 	
	Optimized C++ Multithreaded Programming	
	<ul style="list-style-type: none"> Optimized C++ multithreaded program by techniques such as Loop unrolling, Software Pipelining, Critical Path reduction and Register renaming resulting in speedup over 12 in execution time 	
	Credit Scheduler Implementation [C]	
	<ul style="list-style-type: none"> Designed and implemented credit scheduler with runtime load balancing among processors 	
	Multithreaded Web Server [C]	
	<ul style="list-style-type: none"> Designed and implemented multithreaded WebServer which supports HTTP 1.0 protocol 	
	Addressing Reliability in a Volunteer Computing Setup [Java]	
	<ul style="list-style-type: none"> Designed and implemented a model for distributed processes to execute a real time job reliably 	
	IntellAlarm – An Android based Embedded System [Java]	
	<ul style="list-style-type: none"> An Android application with location based services providing intelligent reminders and navigation 	
Skills	Development Languages: C/C++, Java, x86 assembly, XML-RPC, J2EE Scripting: Unix (bash,csh), Perl Platforms: Linux, Windows Tools: GDB, Apache Tomcat, IntelliJ IDEA, Eclipse, Charles Proxy, JProfiler, JUnit, Perforce	
Courses	- Adv Operating Systems, High Performance Computer Architecture, Real Time Systems, Adv Internet Applications, Computer Networks, Computational Complexity, High Performance Parallel Computing	